

**TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT**  
**(Under 37 CFR 1.97(b) or 1.97(c))**

Docket No.  
11948.21

In Re Application Of: **Rajeev Joshi, et al.**

Serial No.  
10/618,113

Filing Date  
July 11, 2003

Examiner  
n/a

Group Art Unit  
2812

Title: **WAFER LEVEL CHIP SCALE PACKAGE AND METHOD FOR FABRICATING AND USING THE SAME**

Address to:

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**37 CFR 1.97(b)**

1. ☒ The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.

**37 CFR 1.97(c)**

2. ☐ The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:
- ☐ the statement specified in 37 CFR 1.97(e);
- OR**
- ☐ the fee set forth in 37 CFR 1.17(p).

**TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT**  
(Under 37 CFR 1.97(b) or 1.97(c))

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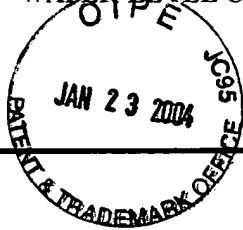
Serial No.  
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July 11, 2003

Examiner  
n/a

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2812

**WAFER-LEVEL CHIP SCALE PACKAGE AND METHOD FOR FABRICATING AND USING THE**



**Payment of Fee**

(Only complete if Applicant elects to pay the fee set forth in 37 CFR 1.17(p))

- ☐ A check in the amount of \_\_\_\_\_ is attached.
- ☐ The Director is hereby authorized to charge and credit Deposit Account No. \_\_\_\_\_ as described below.
- ☐ Charge the amount of \_\_\_\_\_
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I certify that this document and fee is being deposited on 20 January 2004 with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Signature

Dated: January 13, 2004

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application:  
Rajeev Joshi et al.

Serial No.: 10/618,113

Filed: July 11, 2003

For: WAFER-LEVEL CHIP SCALE PACKAGE AND  
METHOD FOR FABRICATING AND USING THE SAME

Confirmation No. 8697

Group Art Unit: 2812

Mail Stop IDS  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Please find, pursuant to 37 C.F.R. § 1.98(a)(1), the enclosed Form PTO-1449 which contains a list of all patents, publications, or other items that have come to the attention of one or more of the individuals designated in 37 C.F.R. § 1.56(c). While no representation is made that any of these references may be "prior art" within the meaning of that term under 35 U.S.C. §§ 102 or 103, the enclosed list of references is disclosed so as to fully comply with the duty of disclosure set forth in 37 C.F.R. § 1.56.

Moreover, while no representation is made that a specific search of office files or patent office records has been conducted or that no better art exists, the undersigned attorney of record believes that the enclosed art is the closest to the claimed invention (taken in its entirety) of which the undersigned is presently aware, and no art which is closer to the claimed invention (taken in its entirety) has been knowingly withheld.

In accordance with 37 C.F.R. §§ 1.97 and 1.98, a copy of each of the listed references or relevant portion thereof is also enclosed (and translation of abstract therewith).

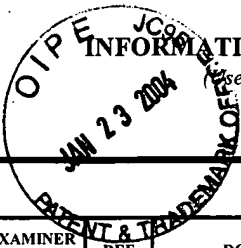
Dated this 13 day of January, 2004.

Respectfully submitted,



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**INFORMATION DISCLOSURE CITATION**

(Use several sheets if necessary)

Docket Number (Optional)

11948.21

Application Number

10/618,113

Applicant(s)

Rajeev R shi, et al.

Filing Date

July 11, 2003

Group Art Unit

2812

**U.S. PATENT DOCUMENTS**

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

**FOREIGN PATENT DOCUMENTS**

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO
	3	2001-0009564	02/05/2001	Korea			✓	
	4	2001-0009429	02/05/2001	Korea			✓	

**OTHER DOCUMENTS** (Including Author, Title, Date, Pertinent Pages, Etc.)


EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.